

ABSTRACT OF THE DISCLOSURE

A multi-path detection circuit can improve path detection accuracy in the environment where the path is located close
5 in the extent of one chip in the multi-path detection circuit of CDMA type receiver unit. The multi-path detection circuit has generating means for generating a logic pattern of a correlated peak in the delay profile, and removing means for removing a power component of the detected correlated peak from
10 the delay profile using the logical pattern of the correlated peak generated by the generating means.

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